TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π -MOSVI)

2SK4012

Switching Regulator Applications

• Low drain-source ON-resistance : $R_{DS (ON)} = 0.33 \Omega (typ.)$

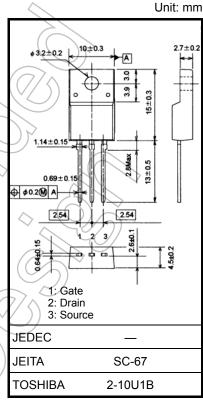
• High forward transfer admittance : |Y_{fs}| = 8.5 S (typ.)

Low leakage current : $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 500 \text{ V)}$

• Enhancement mode : $V_{th} = 2.0 \text{ to } 4.0 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit
Drain-source voltage			V_{DSS}	500	$(\nearrow \land)$
Drain-gate voltage (R _{GS} = 20 kΩ)			V_{DGR}	500	V
Gate-source voltage			V_{GSS}	±30	V
Drain current	DC	(Note 1)	ΙD	13	> A
	Pulse	(Note 1)	I _{DP}	52	Α
Drain power dissipation (Tc = 25°C)			P_{D}	45	W
Single-pulse avalanche energy (Note 2)			EAS	1170	mJ
Avalanche current			IAR	13	A
Repetitive avalanche energy (Note 3)			EAR	<i>)</i> 4.5	mJ
Channel temperature			(T _{ch}	150	∕ °C
Storage temperature range			T _{stg}	-55 to 150	7;¢



Weight: 1.7 (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

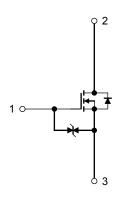
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	2.78	°C/W
Thermal resistance, channel to ambient	R _{th} (ch-a)	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 11.8 mH, R_G = 25 Ω , I_{AR} = 13 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



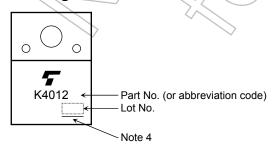
Electrical Characteristics (Ta = 25°C)

Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source br	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cutoff curr	rent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	7	_	100	μΑ
Drain-source b	reakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	_	_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0) /_	4.0	V
Drain-source O	N-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 6.5 A) 	0.33	0.4	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6.5 A	4.0	8.5	_	S
Input capacitan	ce	C _{iss}			2400	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	18	-	pF
Output capacitance		Coss		_	220		
Switching time	Rise time	t _r	10 V I _D = 6.5 A V _{OUT}	- (25	<u> </u>	
	Turn-on time	t _{on}	0 V		70) –	ns
	Fall time	t _f	V _{DD} ≈ 200 V		10	_	
	Turn-off time	t _{off}	Duty ≤ 1%, t _w = 10 μs		95	-	
Total gate charg		Qg	40	_	50	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$	_	30	_	nC
Gate-drain ("Miller") charge		Q _{gd}		_	20	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IÓR	<u> </u>	_	_	13	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	52	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 13 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 13 A, V _{GS} = 0 V	_	1000	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 100 A / μs		11	_	μC

Marking

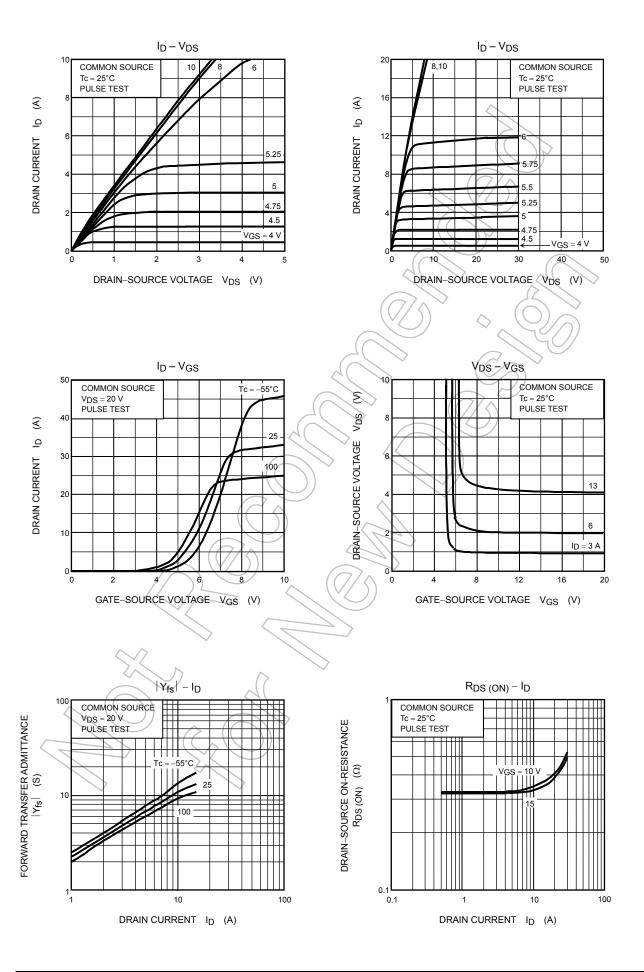


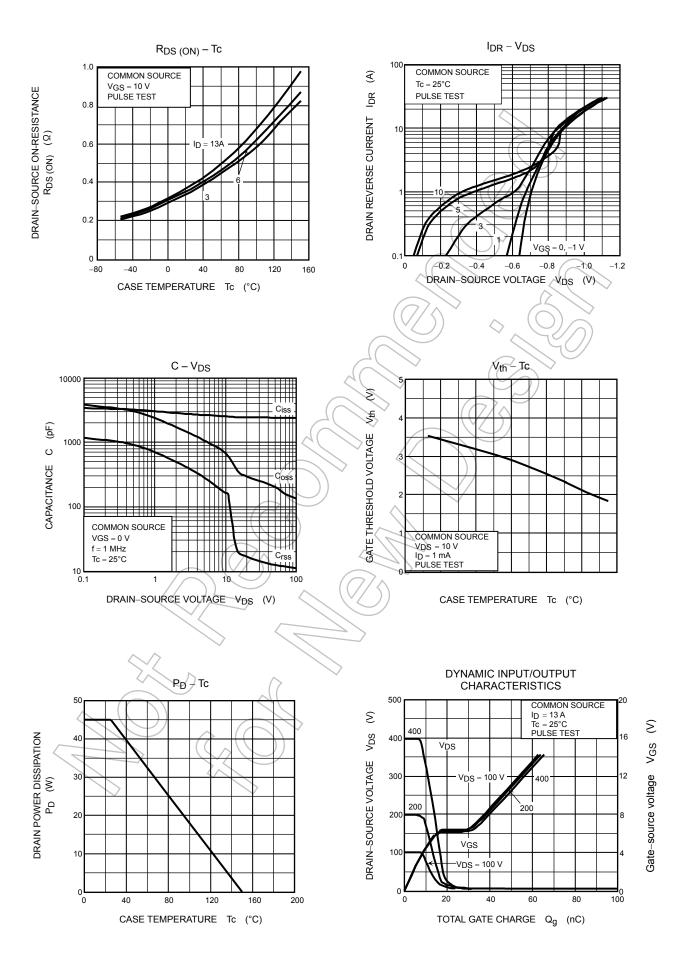
Note 4: A line under a Lot No. identifies the indication of product Labels.

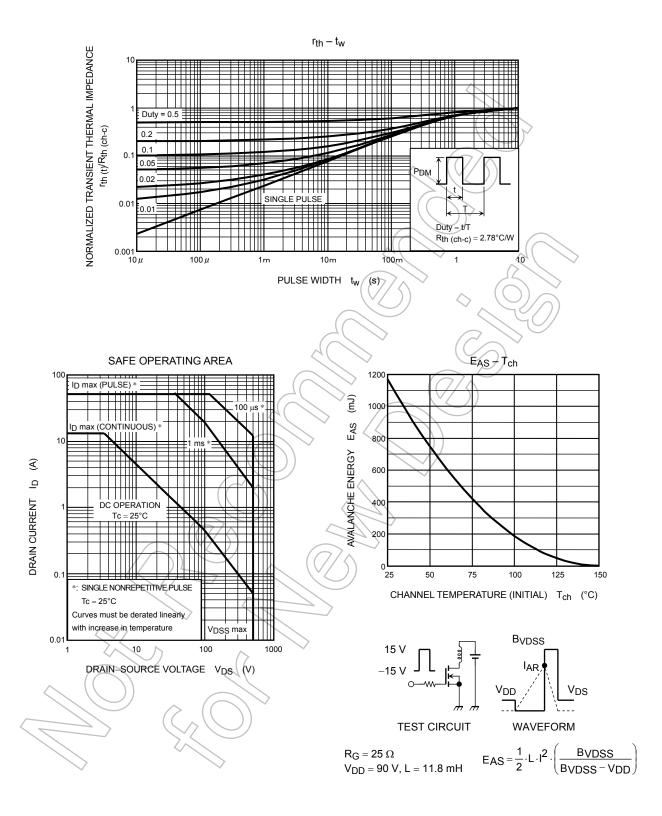
Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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